

CLAIMS

1. An active matrix type liquid crystal display device comprising:
pixel electrodes that are arranged in a matrix and that are driven by pixel transistors
5 respectively;

a plurality of gate lines that are connected, in a column-by-column fashion, to gate electrodes of the pixel transistors;

a plurality of source lines that are connected, in a row-by-row fashion, to source electrodes of the pixel transistors;

10 a gate driver that, sequentially during one selection period after another, connects one of the gate lines after another to an output point of a selection voltage feed circuit; and

a source driver that feeds an image signal to the source lines,

wherein the selection voltage feed circuit has a first power source for feeding a predetermined selection voltage and a second power source for feeding a voltage lower than
15 the predetermined selection voltage, the output point of the selection voltage feed circuit is always fed with the voltage from the second power source, and a switch is provided that so operates that, during a time span that starts at a beginning of every selection period and lasts shorter than the selection period, the output point of the selection voltage feed circuit is fed with the voltage from the first power source.

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2. The active matrix type liquid crystal display device of claim 1,

wherein the second power source is connected via a diode to the output point of the selection voltage feed circuit.

3. The active matrix type liquid crystal display device of claim 2,
wherein the first power source is connected via the switch to the output point of the
selection voltage feed circuit.

5 4. The active matrix type liquid crystal display device of one of claims 1 to 3,
wherein the pixel transistors are formed of amorphous silicon.

5. The active matrix type liquid crystal display device of one of claims 1 to 3,
wherein the selection voltage feed circuit is provided separately from the gate driver.

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6. The active matrix type liquid crystal display device of one of claims 1 to 3,
wherein the selection voltage feed circuit is arranged, along with a low-level gate
voltage source, outside the gate driver.

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7. The active matrix type liquid crystal display device of one of claims 1 to 3,
wherein, as the switch, a plurality of switches are provided one for each gate line, in
parallel with one another.